



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants : Makoto IMANISHI et al.  
Serial No. : 10/761,412 Group Art: Unit : 1725  
Filed : January 22, 2004 Examiner : Michael Aboagye  
For : Method of forming bumps on a wafer utilizing a post-heating operation, and an apparatus therefore

**VERIFICATION OF ENGLISH TRANSLATION**

Honorable Commissioner of Patents and Trademarks  
Washington, D.C. 20231

Sir:

I, Masahiro ONO, declare that I am conversant in both the Japanese and English languages and that the English translation as attached hereto is an accurate translation of Japanese Patent Application No. P10-173086 filed on June 19, 1998.

Signed this 28th day of February, 2007

Masahiro Ono  
Masahiro ONO



PATENT OFFICE  
JAPANESE GOVERNMENT

This is to certify that the annexed is a true copy of the following application as filed with this Office.

Date of Application : June 19, 1998  
Application Number : Patent Application No. 10-173086  
Applicant(s) : Matsushita Electric Industrial Co., Ltd.

June 22, 1999  
Commissioner,  
Patent Office

Takeshi ISAYAMA  
(seal)  
Reg. No. Hei 11-3044234



Document Name: Application for Patent

Docket No.: 161671

Date of Application: June 19, 1998

Addressee: Commissioner, Patent Office

International Patent Classification: H01L 21/60

Title of Invention: BUMP FORMATION METHOD AND BUMP FORMING APPARATUS

Number of Claim(s): 57

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Claiming Priority based on Prior Application

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Item: Abstract 1 copy

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## Applicant Record

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1. Date of Registration: August 28, 1990 (newly recorded)  
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Approved or Supplemented Data

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